FIG. 1

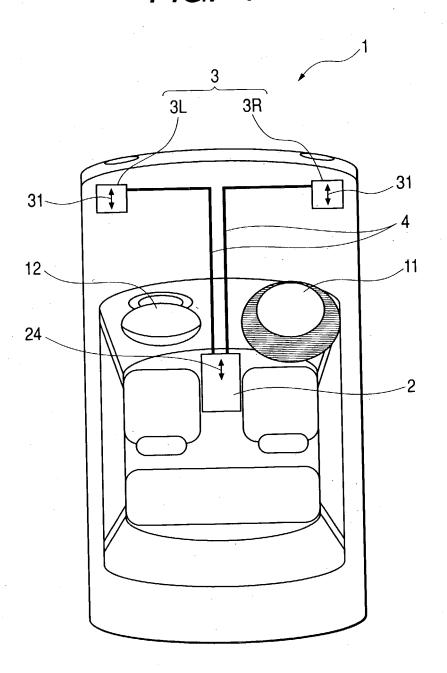
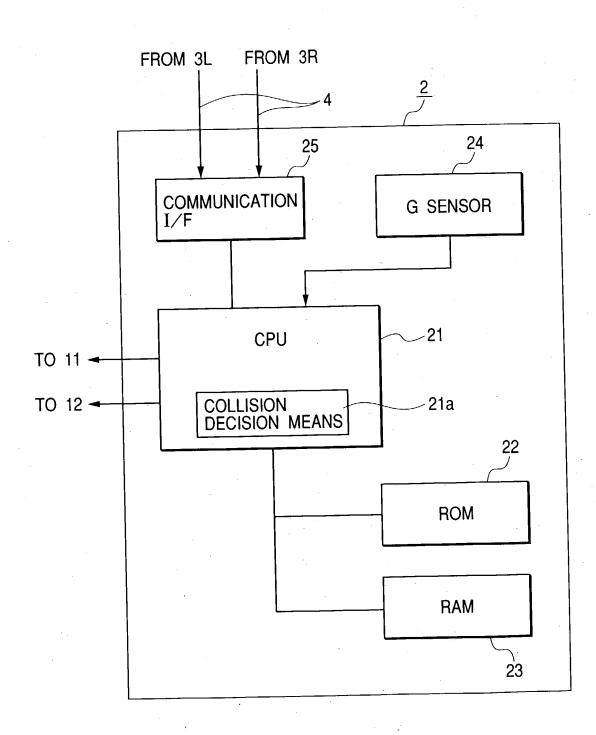


FIG. 2



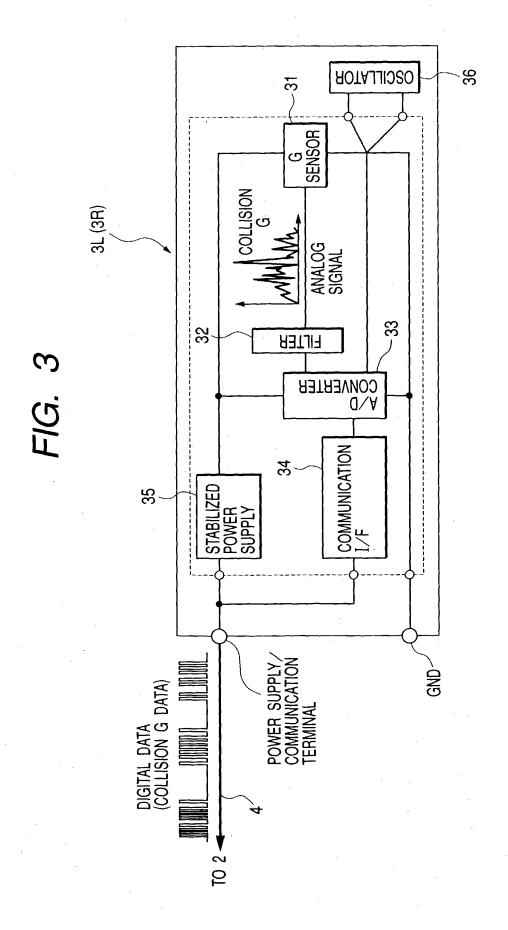
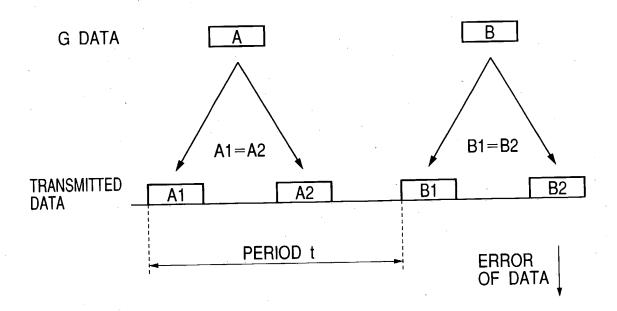


FIG. 4



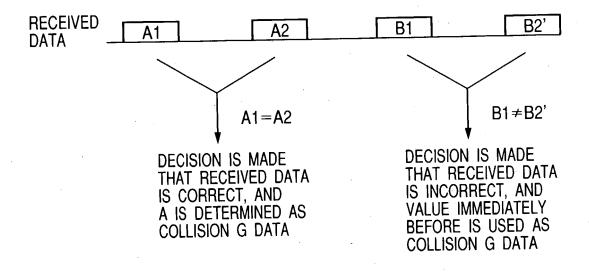


FIG. 5A

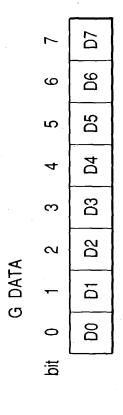


FIG. 5B



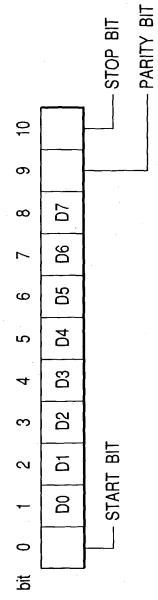


FIG. 6

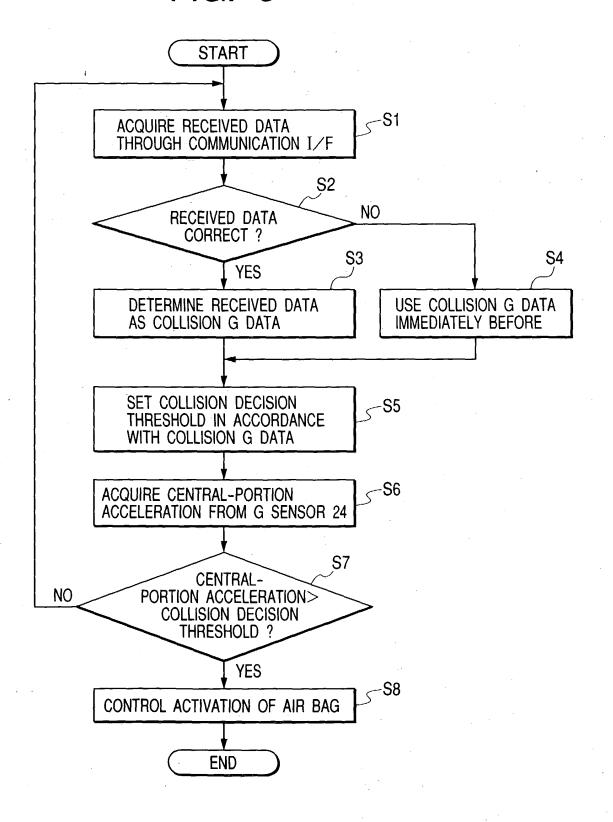


FIG. 7

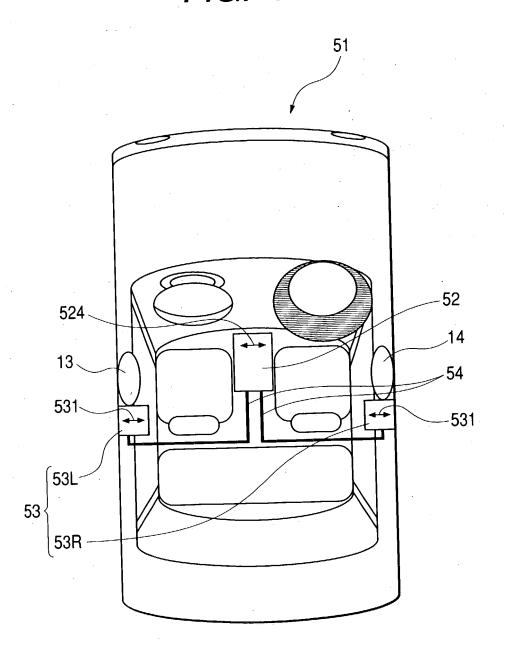


FIG. 8

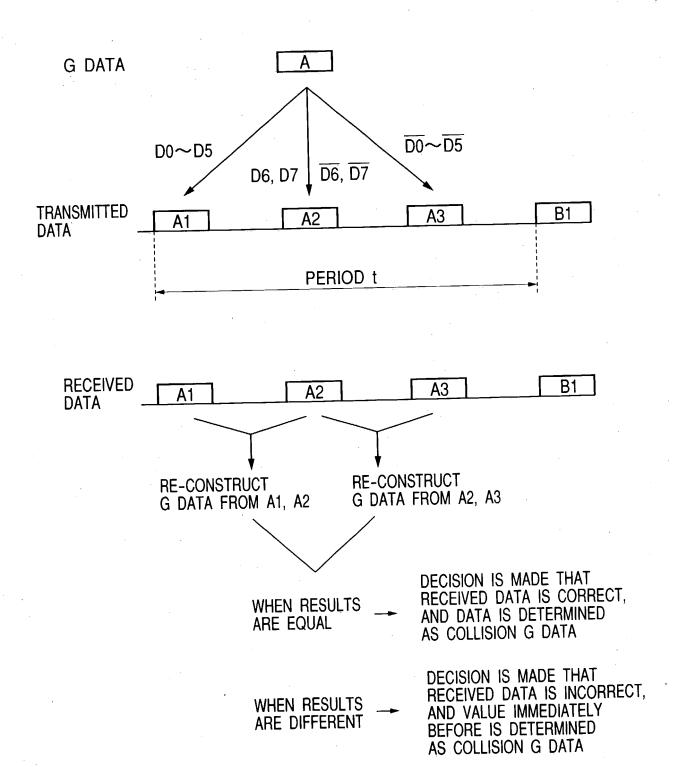


FIG. 9A

ACCELERATION DATA (EX. A)

bit	0	1	2	3	4	5	6	7
	D0	D1	D2	D3	D4	D5	D6	D7

FIG. 9B

FIRST BYTE TRANSMITTED/RECEIVED DATA (EX. A1)

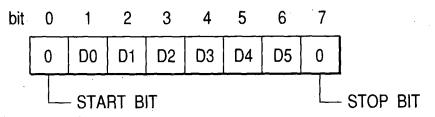


FIG. 9C

SECOND BYTE TRANSMITTED/RECEIVED DATA (EX. A2)

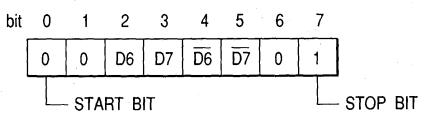


FIG. 9D

THIRD BYTE TRANSMITTED/RECEIVED DATA (EX. A3)

